1.	Name of Course		Microwave Devices and Components									
2.	Course Code			JMDC4522								
	JMDC = the first alphabet identify the faculty within which the subject is offered., JMDC = the remaining three alphabets identify the course that offers the subject, 4 522= the first digit identify level of study; in this case undergraduate level, 4522 = the second and third digits identify subject identity and 4522 = the fourth digit											
	identify credit value or credit hours											
3.	Name(s) of academic staff		To be Assigned									
4.	Rationale for the inclusion of	the cou	rse/mod	dule in t	he	Knowledge Microwave Devices and Components						
	programme			are essential in electrical and electronic								
			engineering. Moreover, acquiring knowledge in									
				Microwave Devices and Components enable								
				engineer to design system and appropriate								
				dielectric system.								
5.	Semester and Year offered					Year 3, Semester 2						
6.	Total Student Learning Time		Eaco +	o Eaco		Total Guided and Independent Learning						
	(SLT)		Face to Face									
	L = Lecture T = Tutorial	L	т	P	IS							
	P = Practical		•	•		Total Guided and Independent Learning = 120						
	IS = Independent Study	42	7	6	65	Total Guidea and macpendent Learning – 120						
	13- macpenaent stady	42	,	0	03							
7.	Credit Value					3.0						
	Lecture: 3 hours per week x 14	l weeks										
	Tutorial: 1 hour per week x 7 v	veeks										
	Practical: 2 hours x 3 weeks											
8.	Prerequisite (if any)					Electrical circuit I and II						
9.	Course Objectives											
	1. This course introduces Microwave Devices and Components and application in Electrical engineering.											

This course introduces Microwave Devices and Components and application in Electrical engineering.

Course Learning Outcomes (CLO)

At the end of the semester students should be able to:

CLO1: To understand and Apply the RF and Microwave design principles;

CLO2: Analyse and design the Microwave and RF Circuits;

CLO3: Understand and Apply the fundamental and advanced communication technologies;

10. Transferable Skills:

This course is expected the development of the following transferable skills:

- An ability to manage time and task
- An ability to learn both independently and co—operatively;
- An ability to take responsibility and carry out laboratory test;
- An ability to take initiative and lead other;
- An ability to use software where relevant to the subject.

11. Teaching-learning and assessment strategy

A variety of learning strategies are used throughout the course, including the following

- Classroom Lesson; Lecturer and power point presentation
- Tutorial session
- Student- lecturer Discussion

- Collaborative and co-operative learn;
- Independent Study.

Assessment:

Course works		40%
Assignment	5%	
Tutorial	5%	
Quizzes	5%	
Laboratory works	10%	
Test	15%	
Final Examination		60%
Total		100%

12. Synopsis:

This course is very important course in the field of electronics engineering. The objective of course is to expose students to engineering system development process and to enhance students' skill and capability in understanding microwaves components.

13. Mode of Delivery:

Lectures;

Tutorials;

Laboratory works

CLO-PLO	ia : Assessment Tool	1	2	3	4	5	
Marks		0-39	40-49	50-59	60-74	75-100 (A-,A,A+ Excellent To -learn both independer and cooperative -take responsibili and carry o	
Grade		(F)	(D,D+)	(C-,C,C+)	(B-,B,B+)		
CLO1: To understand and Apply the RF and Microwave design principles;	Assignment Tutorials Quizzes Lab works Test Examination	Fail To: -learn both independently and cooperatively -take responsibility and carry out	Poor To: -learn both independently and cooperatively -take responsibility and carry out	Satisfactory To: -learn both independently and cooperatively -take responsibility and carry out	Good To: -learn both independentl y and cooperatively -take responsibility and carry out		
		laboratory test	laboratory test	laboratory test	laboratory test	laboratory	
CLO2: To Analyse and design the Microwave and RF Circuits;	Assignment Tutorials Quizzes Lab work Test Examination	Fail To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out laboratory test	Poor To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out laboratory test	Satisfactory To: -manage time and task -learn both independently and cooperatively -take responsibility and carry out laboratory test	Good To: -manage time and task -learn both independentl y and cooperatively -take responsibility and carry out laboratory test	- manage tir and task - learn both independe and cooperativ - take responsibil and carry of laboratory	
CLO3: Understand and Apply the fundamental and advanced communication technologies;	Assignment Tutorials Quizzes Lab work Test Examination	Fail To: -manage time and task -learn both independently and cooperatively -take responsibility and carry out laboratory test -take responsibility and carry out	Poor To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out laboratory test - take responsibility and carry out	Satisfactory To: -manage time and task -learn both independently and cooperatively -take responsibility and carry out laboratory test -take responsibility and carry out	Good To: -manage time and task -learn both independentl y and cooperatively -take responsibility and carry out laboratory test -take responsibility and carry out laboratory	Excellent To - manage tin and task - learn both independer and cooperative - take responsibili and carry of laboratory - take responsibili and carry of laboratory	

15.	Mapping of the Programme Objectives to the Programme Learning Outcomes											
	Programme Learning Outcomes (PLO) Programme Objectives (PO)	PLO1: Ability to acquire and apply knowledge of science and engineering fundamentals.	PLO2: Acquired in-depth technical competence in electronics engineering discipline.	PLO3: Ability to undertake problem identification, formulation and solution;	PLO4: Ability to utilise systems approach to design and evaluate operational performance.	PLO5: Understanding of the principles of design for sustainable development;	PLO6: Understanding of professional and ethical responsibilities and commitment to them.	PLO7: Ability to communicate effectively, not only with engineers but also with the community at large.	PLO8: Ability to function effectively as an individual and in a group with the capacity to be a leader or manager;	PLO9: Understanding of the social, cultural, global and environmental responsibilities of a professional engineer	PLO10: Recognising the need to undertake lifelong learning, and possessing/acquiring the capacity to do so	PLO11: Ability become entrepreneur
	PEO1: To produce graduates with excellent knowledge and competency in Electrical and Electronic Engineering;	√	✓							✓	✓	
	PEO2: To produce graduates with professional, generic attributes to meet the present and future global demands.											
	PEO3: To produce graduates with Islamic humanistic values and reinvention skills to meet the requirement of a dynamic environment. These skills include Civil Intelligence, Moral Intelligence, Self-Reliance and Communication Skills.											

16. Mapping of the course Learning Out	come to	o the Pr	ogramn	ne Outc	ome						
Course Learning Outcome (CLO)	PLO1: Ability to acquire and apply knowledge of science and engineering fundamentals.	PLO2: Acquired in-depth technical competence in electronics engineering discipline.	PLO3: Ability to undertake problem identification, formulation and solution;	PLO4: Ability to utilise systems approach to design and evaluate operational performance.	PLO5: Understanding of the principles of design for sustainable development;	PLO6: Understanding of professional and ethical responsibilities and commitment to them.	PLO7: Ability to communicate effectively, not only with engineers but also with the community at large.	PLO8: Ability to function effectively as an individual and in a group with the capacity to be a leader or manager;	PLO9: Understanding of the social, cultural, global and environmental responsibilities of a professional engineer	PLO10: Recognising the need to undertake lifelong learning, and possessing/acquiring the capacity to do so	PLO11: Ability become entrepreneur
CLO1: To understand and Apply the RF and Microwave design principles;	√	✓							√	✓	
CLO2: To Analyse and design the Microwave and RF Circuits;	✓	√							✓	✓	
CLO3: Understand and Apply the fundamental and advanced communication technologies;	✓	√							✓	✓	

D				SLT (Hou	r)		
Deta	IIIS	L	Т	Р	IS	Tot	
	Review:						
Topic 1	Maxwell's equations and transmission line theory – review.	3	-	-	6		
	Microwave Network Analysis:						
Topic 2	Analyse Transmission line equations and solutions - Smith chart, ABCD Matrix, S-Parameter Matrix and Signal Flow Graphs	6	1	-	9	1	
	Impedance Transformation and Matching:						
Topic 3	Impedance Measurements of impedance, Single-Stub Matching, Double stub- matching, Triple-Stub matching, impedance matching with Lumped elements, Waveguide reactive elements, Quarter- wave transformer, Binomial Transformer, Chebyshev Transformer, Tapered Transmission lines	9	2	-	14	2	
	Waveguide components:						
Topic 4	Introduction to Rectangular waveguide, Bends and Twists, Ridge waveguide, Fin Line, Terminations, Attenuators, Phase Shifters, Circular Polarizers, Directional Couplers, Hybrid Junctions, Ferrites, Faraday Rotations, Isolators, Circulators, Cavity Resonators, Coaxiato-waveguide Transitions, Rotary Joints and Switches.	9	1	-	14	2	
	Coaxial Components:						
Topic 5	Two-wire line, Coaxial cables, Terminations, Connectors and Transitions, Attenuators, Phase Shifters and Baluns	6	1	-	9	1	
	Stripline Circuits:						
Topic6	Introduction to Substrate Materials, Stripline, Coupled Stripline, Microstrip, Coupled Microstrip, Coplanar stripline, Terminations, Attenuators, Coupled, Power dividers, Circulators and Isolators, Resonators, Lowpass Filters, Bandstop Filters, Bandpass Filters and Wideband Filters,	9	2	-	13	2	
practical	Comparison of different materials dielectric Impedance measurement Wire Terminations	-	-	6	-		
	Total SLT (Hour)	42	7	6	65	13	

18.	8. Main references supporting the course						
	1. David M. Pozar, "Microwave Engineering", 3 rd Edition, Wiley, 2009						
	Additional references supporting the course						
19.	Other additional information						
	All materials will be available to the students in the library.						