Name of Course					Power system I					
Course Code					JPWR1113					
<b>JPWR</b> = the first alphabet identify	the facu	lty with	nin whi	ch the	subject is offered., <b>JPWR</b> = the remaining three alphabet identify					
the course that offers the subject,	<b>1</b> 113 = t	the first	t digit i	level of study; in this case undergraduate level, 1113 = the second						
and third digits identify subject identity and 1113 = the fourth digit identify credit value or credit hours										
Name(s) of academic staff					To be Assigned					
Rationale for the inclusion of the	ne cours	se/mo	dule ii	n the	Knowledge of power system is essential in power					
programme				engineering. Moreover, acquiring knowledge in						
				interconnection power system enable engineer to design						
				generation and transmission system.						
Semester and Year offered				Year 3, Semester 1						
Total Student Learning Time (SLT)	Face to Face				Total Guided and Independent Learning					
L = Lecture T = Tutorial  P = Practical	L	Т	Р	IS	Total Guided and Independent Learning = 120					
IS= Independent Study	42	7	6	65	rotal duided and independent Learning – 120					
Credit Value				3.0						
Lecture: 3 hours per week x 14	weeks									
Tutorial: 1 hour per week x 7 we	eeks									
Practical: 2 hours x 3 weeks										
Prerequisite (if any)					Electric circuit II					
	Course Code  JPWR = the first alphabet identify the course that offers the subject, and third digits identify subject ide  Name(s) of academic staff  Rationale for the inclusion of the programme  Semester and Year offered  Total Student Learning Time (SLT)  L = Lecture	Course Code  JPWR = the first alphabet identify the faculthe course that offers the subject, 1113 = the and third digits identify subject identity and Name(s) of academic staff Rationale for the inclusion of the course programme  Semester and Year offered Total Student Learning Time (SLT)  L = Lecture	Course Code  JPWR = the first alphabet identify the faculty with the course that offers the subject, 1113 = the first and third digits identify subject identity and 1113  Name(s) of academic staff  Rationale for the inclusion of the course/morprogramme  Semester and Year offered  Total Student Learning Time (SLT)  L = Lecture	Course Code  JPWR = the first alphabet identify the faculty within whith the course that offers the subject, 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits in the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify and 1113 = the first digit is and third digits identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit is and third digits identify subject identity and 1113 = the first digit in the first digit identity and 1113 =	Course Code  JPWR = the first alphabet identify the faculty within which the the course that offers the subject, 1113 = the first digit identify and third digits identify subject identity and 1113 = the fourth of the Name(s) of academic staff  Rationale for the inclusion of the course/module in the programme  Semester and Year offered  Total Student Learning Time (SLT)  L = Lecture					

#### 9. **Course Objectives**

1. To equip the students with the knowledge of the interconnection systems.

## **Course Learning Outcomes (CLO)**

At the end of the semester students should be able to:

CLO1: State the advantages and disadvantages power system;

CLO2: To explain the construction of power circuit diagram and the techniques for reducing interconnected system to simple equivalents;

CLO3: To explain the various types of faults that occur on generators and transformers.

### 10. Transferable Skills:

This course is expected the development of the following transferable skills:

- An ability to manage time and task
- An ability to learn both independently and co—operatively;
- An ability to take responsibility and carry out laboratory test;
- An ability to take initiative and lead other;
- An ability to use software where relevant to the subject.

## 11. Teaching-learning and assessment strategy

A variety of learning strategies are used throughout the course, including the following

- Classroom Lesson; Lecturer and power point presentation
- Tutorial session
- Student- lecturer Discussion
- Collaborative and co-operative learn;
- Independent study.

	Assessment:			
	Course works		40%	
	Assignment	5%		
	Tutorial	5%		
	Quizzes	5%		
	Laboratory works	10%		
	Test	15%		
	Final Examination		60%	
	<u>Total</u>		<u>100%</u>	
12.	Synopsis:			
	This course is very important of	ourse in the	field of elec	trical engineering. The objective of course is to equip student
	with the knowledge of interco	nnection pov	ver system.	

# 13. Mode of Delivery:

Lectures;

Tutorials;

Laboratory works

CLO-PLO	Assessment Tool	1	2	3	4	5	
Marks		0-39	40-49	50-59	60-74	75-100	
Grade		(F)	(D,D+)	(C-,C,C+)	(B-,B,B+)	(A-,A,A+	
CLO1:  State the advantages and disadvantages power system;	Assignment Tutorials Lab works Quizzes Test Examination	Fail To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out	Poor To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out	Satisfactory To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out	Good To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out	- manage tir and task - learn both independe and cooperativ - take responsibi and carry (	
CLO2:  To explain the construction of power circuit diagram and the techniques for reducing interconnected system to simple equivalents;	Assignment Tutorials Lab works Test Quizzes Examination	laboratory test  Fail To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out laboratory test	laboratory test  Poor To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out laboratory test	laboratory test  Satisfactory To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out laboratory test	laboratory test  Good To: - manage time and task - learn both independently and cooperatively - take responsibility and carry out laboratory test	laboratory  Excellent To  - manage ti and task  - learn both independe and cooperativ  - take responsibi and carry of laboratory	
CLO3:  To explain the various types of faults that occur on generators and transformers;	Assignment Tutorials Lab works Quizzes Test Examination	Fail To: - manage time and task - learn both independently and cooperatively - take responsibility	Poor To: - manage time and task - learn both independently and cooperatively - take responsibility	Satisfactory To: - manage time and task - learn both independently and cooperatively - take responsibility	Good To: - manage time and task - learn both independently and cooperatively - take responsibility	Excellent T - manage tii and task - learn both independe and cooperativ - take responsibi	

15.	Mapping of the Programme Obje	ectives to	the Pro	gramme	e Learni	ng Outc	omes					
15.	Programme Learning Outcomes (PLO)	PLO1: Ability to acquire and apply knowledge of science and apply and engineering fundamentals.	PLO2: Acquired in-depth technical competence in electrical engineering discipline.	PLO3: Ability to undertake problem identification, but formulation and solution;	PLO4: Ability to utilise systems approach to design and a evaluate operational performance.	PLOS: Understanding of the principles of design for OS sustainable development;	PLO6: Understanding of professional and ethical         a         b         a         a         a         a         a         a         a         a         a         a         a         a         b         a         a         a         a         a         a         a         a         a         a         a         a	<b>PLO7:</b> Ability to communicate effectively, not only with engineers but also with the community at large.	<b>PLO8:</b> Ability to function effectively as an individual and in a group with the capacity to be a leader or manager;	<b>PLO9:</b> Understanding of the social, cultural, global and environmental responsibilities of a professional engineer	<b>PLO10:</b> Recognising the need to undertake lifelong learning, and possessing/acquiring the capacity to do so	PL011: Ability become entrepreneur
	Programme Objectives(PO)  PEO1: To produce graduates with excellent knowledge and competency in Electrical and Electronic Engineering	PLO1: Abil and engine	PLO2: Acc	PLO3: Abil formulatio	PLO4: Abil evaluate o	<b>PLO5:</b> Und sustainable	<b>PLO6:</b> Und responsibi	PLO7: Abil engineers	PLO8: Abil a group wi	PLO9: Und environme	PLO10: Re learning, a	<b>PLO11</b> : Ab
	<b>PEO2:</b> To produce graduates with professional, generic attributes to meet the present and future global demands.			✓				<b>√</b>				
	PEO3: To produce graduates with Islamic humanistic values and reinvention skills to meet the requirement of a dynamic environment. These skills include Civil Intelligence, Moral Intelligence, Self-Reliance and Communication Skills;							<b>√</b>				

16. Mapping of the course L											
Programme Learning Outcomes (PLC	ng	<b>PLO2:</b> Acquired in-depth technical competence in electrical engineering discipline.	<b>PLO3:</b> Ability to undertake problem identification, formulation and solution;	<b>PLO4:</b> Ability to utilise systems approach to design and evaluate operational performance.	<b>PLO5:</b> Understanding of the principles of design for sustainable development;	<b>PLO6:</b> Understanding of professional and ethical responsibilities and commitment to them.	<b>PLO7:</b> Ability to communicate effectively, not only with engineers but also with the community at large.	<b>PLO8:</b> Ability to function effectively as an individual and in a group with the capacity to be a leader or manager;	<b>PLO9:</b> Understanding of the social, cultural, global and environmental responsibilities of a professional engineer	PLO10: Recognising the need to undertake lifelong learning, and possessing/acquiring the capacity to do so	PLO11: Ability become entrepreneur
CLO1: State the advantages as disadvantages pow system;	v	<b>✓</b>		✓			<b>√</b>			✓	
CLO2:  To explain the construction of power circuit diagram at the techniques for reduction interconnected system simple equivalents;	nd 🗸	<b>✓</b>		<b>✓</b>			<b>✓</b>			<b>√</b>	
CLO3:  To explain the various typ  of faults that occur  generators and transforme	on V	<b>✓</b>		✓			<b>✓</b>			<b>√</b>	

D - 4 -	.91.			SLT (Hour)		
Deta	alis	L	Т	Р	IS	T
Topic 1	Introduction Interconnection to power system	12	2	-	16	:
Topic 2	<ul> <li>Generator and transformer fault</li> <li>Generator and transformer fault</li> <li>Transmission line fault</li> <li>Short circuit, open circuit and earth and earth fault in lines.</li> </ul>	6	1	-	11	:
Topic3	<ul> <li>Positive , negative and zero sequence components</li> <li>Symmetrical and unsymmetrical fault</li> <li>Switchgear         <ul> <li>Bus-bar and fuses</li> </ul> </li> </ul>	6	1	-	11	
Topic 4	Corona Affect      Factors affecting corona     Over-load voltage     Surge voltage     Causes for surge voltage     Uniform line	6	1	-	11	,
Topic 5	Surge impedance     Line termination     Transmission coefficient     Overhead lines cables and insulator     Line equation     Fault finding and line testing Impulse generator	12	2	-	16	3
Practical	<ol> <li>Experiment on characteristics of impulse generator</li> <li>Test insulators using various methods.</li> </ol>	-	-	6	-	
	Total SLT (Hour)	42	7	6	65	1
:	n references supporting the course 1. Electric power system,. By James.A. Momoh 2009. 2. Electri Power system: Design and Analysis by Mohammed itional references supporting the course	d El-Hawary la	atest ed.			

All materials will be available to the students in the library.